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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/941,057	08/28/2001	Steven W. Rogers	5150-59901	5661

35690 7590 05/05/2004

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EXAMINER

KENDALL, CHUCK O

ART UNIT	PAPER NUMBER
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2122

DATE MAILED: 05/05/2004

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Please find below and/or attached an Office communication concerning this application or proceeding.

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## Office Action Summary

Application No.

09/941,057

Applicant(s)

ROGERS ET AL.

Examiner

Chuck Kendall

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

### A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 08/28/2001, 01/04/02.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-36 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 4.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

### DETAILED ACTION

1. This action is in response to the application filed 08/28/01.
2. Claims 1 – 36 have been examined.

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

4. Claims 1 – 7 & 9 – 36 are rejected under 35 U.S.C. 102(e) as being anticipated by Leask et al. USPN 6,412,106 B1 (hereinafter "Leask").

Regarding claim 1, Leask anticipates a computer-implemented method for creating a graphical program, the method comprising:

creating a first graphical program and storing the first graphical program in a memory (10:1 – 5, see graphical program to be debugged, running either locally or remotely);

associating a debugging graphical program at a debugging location in the first graphical program, wherein said associating does not modify the first graphical program (10:1 – 5, see graphical debugger program 100, which is also either local or remote);

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wherein the debugging graphical program is executable during execution of the first graphical program to aid in debugging at least a portion of the first graphical program (10: 5 – 10, see “utilized to debug locally stored programs or programs stored at remote locations”).

Regarding claim 2, the method of claim 1, wherein said associating does not require a recompilation of the first graphical program (5: 15 – 20, shows prior art teaches away from recompilation).

Regarding claim 3, the method of claim 1, further comprising:  
executing the first graphical program up to the debugging location (10:1 – 5);  
executing the debugging graphical program after executing the first graphical program up to the debugging location (10:1 – 5); and

the debugging graphical program generating debugging results, wherein the debugging results are useful in analyzing at least a portion of the first graphical program (12:50 – 57).

Regarding claim 4, the method of claim 3, further comprising:  
completing execution of the first graphical program based on the debugging results of said executing the debugging graphical program (7:30 – 35).

Regarding claim 5, the method of claim 3, wherein said executing the debugging graphical program includes displaying the debugging results of the debugging graphical program (7: 8 – 20).

Regarding claim 6, the method of claim 3, wherein said executing the debugging graphical program comprises:

receiving data from the first graphical program (19:30 – 40, see request and receive); and

performing one or more of displaying the data from the first graphical program (21: 40 – 43);

and/or logging the data from the first graphical program to a file (26: 54, for logging see memory for storing).

Regarding claim 7, the method of claim 3, wherein said executing the debugging graphical program comprises:

receiving data from the first graphical program (10: 5 – 10);

generating statistics based on the received data (3:15 – 23); and

displaying the statistics (7: 7– 10, see application program to be debugged is displayed);

Regarding claim 9, the method of claim 3, wherein said completing execution of the first graphical program is performed in single stepping mode based on the debugging results of said executing the debugging graphical program (FIG. 5, 414, & 416, see associated text also).

Regarding claim 10, the method of claim 1, further comprising:

executing the first graphical program up to the debugging location, wherein the first graphical program generates data at the debugging location (10:5 – 10);

providing the data to the debugging graphical program (3:15 – 23, & 10:1 – 5);

executing the debugging graphical program, wherein the debugging graphical program uses the data (3:15 – 23, also see 7: 7– 10);

the debugging graphical program generating debugging results (7: 7– 10);

based on the debugging results, performing one or more of:

halting execution of the first graphical program (FIG.9, 914);

entering single stepping mode in the first graphical program (FIG. 9, 916); or

completing execution of the first graphical program (FIG.5, see 414 & 416).

Regarding claim 11, the method of claim 10, wherein the first graphical program executes up to the debugging location where the debugging graphical program is associated, and waits for user input (FIG.9, 918).

Regarding claim 12, the method of claim 1, wherein the first graphical program comprises a plurality of interconnected nodes, which visually indicate functionality of the first graphical program (7: 10 – 12, see interconnected icons).

Regarding claim 13, the method of claim 12, wherein the first graphical program comprises a plurality of data flow paths (12:18 – 20, call flow);

wherein said associating a debugging graphical program at a location in the first graphical program comprises associating the debugging graphical program at a first data flow path in the first graphical program (12: 20 – 25).

Regarding claim 14, the method of claim 13, wherein said associating comprises: storing information in at least one data structure, wherein the information comprises information regarding the first graphical program, the debugging graphical program, and the location where the debugging graphical program is attached

along the first data flow path of the first graphical program (10:1 – 10).

Regarding claim 15, the method of claim 13, wherein said associating comprises:  
receiving user input from a pointing device selecting a data flow path in the first graphical program;

display a plurality of debugging graphical programs (21: 40 – 43, and 55 – 60, for multiple debug tools); and

receiving user input selecting one of the debugging graphical programs (21: 47 – 50, see touch tones for user input).

Regarding claim 16, the method of claim 13, wherein said associating comprises:  
receiving user input selecting a data flow path in the first graphical program, wherein the data flow path is configured to carry data of a first data type (FIG.8, 810, and associated text);

determining the first data type of the data flow path display a plurality of debugging graphical programs appropriate for the first data type of the data flow path (21: 40 – 43, and 55 – 60, for multiple debug tools); and

Regarding claim 17, the method of claim 13, wherein said associating the debugging graphical program at the debugging location in the first graphical program comprises associating the debugging graphical program at a node in the first graphical Program (10: 1 – 5, for node see local).

Regarding claim 18, the method of claim 1, further comprising:

disassociating the debugging graphical program from the first graphical program, wherein said disassociating does not modify the first graphical program and/or does

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not require a re-compilation of the first graphical program (5: 15 – 20, shows prior art teaches away from recompilation).

Regarding claim 19, the method of claim 1, wherein the first graphical program is located on a first computer system;

wherein the debugging graphical program is located on a second computer system, wherein the second computer system is coupled to the first computer system over a network (10: 1 – 10, see local and remote).

Regarding claim 20, the method of claim 19, the method further comprising: executing the first graphical program on the first computer system up to the debugging location;

executing the debugging graphical program on the second computer system, wherein the debugging graphical program is executed after executing the first graphical program on the first computer system up to the debugging location (10: 1 – 10);

the debugging graphical program generating debugging results on the second computer system (10: 5 – 10); and

providing the debugging results from the second computer system to the first computer system (10: 5 – 10, also see FIG. 7, 708 & 710, for send and receive data between debug engine and user application (graphics program)).

Regarding claim 21, the method of claim 1, wherein the first graphical program is located on a first computer system, wherein the first computer system is a target



computer system coupled to or comprised in a second computer system (10: 1 – 10, see local and remote);

wherein the debugging graphical program is located on and executed on the first computer system (10: 1 – 10, see local and remote as well as debug program and graphics program).

Regarding claim 22, the method of claim 1, wherein the first graphical program is located on a first computer system, wherein the first computer system is a target computer system coupled to or comprised in a second computer system (10: 1 – 10, see local and remote);

wherein the debugging graphical program is located on and executed on the second computer system (10: 1 – 10, see local and remote, teaches the debug tool to be either remotely or locally located).

Regarding claim 23, the computer-implemented method, recites similar limitations as in claim 2, see rationale as previously discussed above.

Regarding claim 24, the computer-implemented method, recites similar limitations as in claim 10, see rationale as previously discussed above.

Regarding claim 25, the computer-implemented method, also recites similar limitations as in claim 2, see rationale as previously discussed above.

Regarding claim 26, the computer-implemented method, recites similar limitations as in claim 1, see rationale as previously discussed above.

Regarding claim 27, which is also a method claim, recites similar limitations as in claim 1, see rationale as previously discussed above.

Regarding claim 28, which is also a method claim, recites similar limitations as in claim 2, see rationale as previously discussed above.

Regarding claim 29, which is also a method claim, recites similar limitations as in claim 3, see rationale as previously discussed above.

Regarding claim 30, which is also a method claim, recites similar limitations as in claim 10, see rationale as previously discussed above.

Regarding claim 31, which is the memory medium version of claim 1, see rationale as previously discussed above.

Regarding claim 32, which is the memory medium version of claim 2, see rationale as previously discussed above.

Regarding claim 33, which is the memory medium version of claim 3, see rationale as previously discussed above.

Regarding claim 34, which is the memory medium version of claim 10, see rationale as previously discussed above.

Regarding claim 35, which is also a memory medium claim, recites similar limitations as in claim 10, see rationale as previously discussed above.

Regarding claim 36, which is also a memory medium claim, recites similar limitations as in claim 10, see rationale as previously discussed above.

### ***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Leask et al. USPN 6,412,106 B1 (hereinafter "Leask"), as applied in claim 8 in view of McKee et al. USPN 5,915,114 (hereinafter " McKee").

Regarding claim 8, Leask discloses all the claimed limitations as applied in claim 7 above. Leask doesn't explicitly disclose including differences in execution times between the plurality of executions of the debugging graphical program, wherein said differences in execution times are useable in optimizing performance. However, McKee discloses analyzing " the code being executed for non-optimal instructions streams and modifies the code in real time in order to generate optimized object code that is capable of enhanced performance for the given data being run with the program" (3: 42 – 47). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine Leask and McKee because, it would have enabled the code to be optimized.

### **Correspondence Information**

7. Any inquires concerning this communication or earlier communications from the examiner should be directed to Chuck O. Kendall who may be reached via telephone at (703) 308-6608. The examiner can normally be reached Monday through Friday between 8:00 A.M. and 5:00 P.M. est.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Dam *can be* reached at (703) 305-4552.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

*For facsimile (fax) send to 703-7467239 official and 703-7467240*  
*draft*

*Chuck D. Kendall*

*Software Engineer Patent Examiner  
United States Department of Commerce*



**WEI Y. ZHEN  
PRIMARY PATENT EXAMINER**